

IBM Docket No. RPS920010126US1

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by Karen Orzechowski

Signature:

Karen Orzechowski

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date: April 8, 2002

In re Application of: R. T. Bailis, et al.

Serial Number: 10/016,772

Filed: 12/10/2001

For: Method and System for Use of an Embedded Field Programmable Gate Array Interconnect for Flexible I/O Connectivity

Art Unit: 2819

Examiner:

JUN 13 2002
TECHNOLOGY CENTER 2800

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Assistant Commissioner for Patents
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Sir:

Applicant hereby requests that a Corrected Filing Receipt be issued for the above-identified patent application. Please amend the Filing Receipt. The "Atty. Docket No." should be -- RPS920010126US1 -- as it was on the original Filing Receipt. A photocopy of the Filing Receipt with the requested changes noted thereon is enclosed for reference.

Respectfully submitted,

By *Joscelyn G. Cockburn*

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JGC/ko
Enclosures



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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/016,772	12/10/2001	2819	908	RPS920010134US1	3	11	5

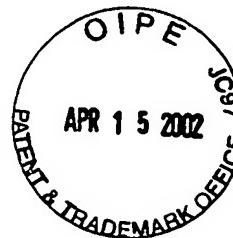
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CONFIRMATION NO. 3353

UPDATED FILING RECEIPT

OC00000007699712*

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IBM Corporation 9CCA/B002
PO Box 12195
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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant**Foreign Applications**

02 MAR 28 ADO:27

If Required, Foreign Filing License Granted 01/15/2002

PC COMPANY
LEGAL DEPARTMENT
RALEIGH

Projected Publication Date: 06/12/2003**Non-Publication Request:** No**Early Publication Request:** No**Title**

Method and system for use of an embedded field programmable gate array interconnect for

flexible I/O connectivity

Preliminary Class

326



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